# NEPAL ENGINEERING COUNCIL



Concept of Basic Electrical and Electronics Engineering

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1.6 Amplifiers: Classification of Output Stages, Class A Output Stage, Class B Output Stage, Class AB Output Stage, Biasing the Class AB Stage, Power BJTs, Transformer-Coupled Push-Pull Stages, and Tuned Amplifiers, op-amps.

### **Operational Amplifier**

- An operational amplifier (Op-Amp) is a differential amplifier that amplifies the difference of voltages applied to its two input terminals (differential input), and provides a single- ended output.
- Amplifiers are devices which take a relatively weak signal as an input and produce a much stronger signal as an output.
- V+: non-inverting input
- V-: inverting input
- Vout: output
- VS+: positive power supply
- VS-: negative power supply





Pin Number	Pin Name	Pin Function
1	Offset Null	This pin is used to eliminate offset
		voltage and balance the input
		voltages
2	Inverting Input	Inverting Signal Input
3	Non-Inverting	Non-inverting Signal Input
	Input	
4	Negative	Negative Supply Voltage
	Supply Voltage	
5	Offset Null	This pin is used to eliminate offset
		voltage and balance the input
		voltages
6	Output	Amplified Signal Output
7	Positive	Positive Supply Voltage
	Supply Voltage	
8	NC	Not Connected

#### IDEAL And PRACTICAL Characteristics Of Op-Amp

characteristics	Practical value	Ideal value
Voltage gain	2 ×10 <sup>5</sup>	∞
Input resistance	2ΜΩ	∞
Output resistance	75Ω	0
Bandwidth	1 MHz	∞
CMRR	90 dB	∞
Slew rates	0.5V/µs	∞
PSRR	150µV/V	0
Input offset voltage	2mV	0
Input bias current	50 nA	0
Input offset current	6 nA	0

#### Virtual Short Circuit and Virtual Ground



#### **Inverting Amplifier**

- Inverting amplifier is one in which the output is exactly 180 degree out of phase with respect to input(i.e. if you apply a positive voltage, output will be negative).
- Output is an inverted(in terms of phase) amplified version of input.



KCL at node 1:  $(V_i - 0) / R_1 = (0 - V_o) / R_2$  $V_i / R_1 = - V_o / R_2$ 

$$\frac{V_o}{V_i} = \frac{R_2}{R_1}$$

Voltage gain, 
$$A_v = \frac{V_0}{V_i} = -\frac{R_2}{R_1}$$

### Non Inverting Amplifier

- Non Inverting amplifier is one in which the output is in phase with respect to input(i.e. if you apply a positive voltage, output will be positive ).
- Output is an Non inverted(in terms of phase) amplified version of input.

Voltage at node 1 = voltage at node 2 🛓 KCL at node 1:  $(0 - V_i) / R_1 = (V_i - V_o) / R_2$  $-(V_i / R_1) = (V_i / R_2) - (V_0 / R_2)$  $V_o / R_2 = (V_i / R_2) + (V_i / R_1) = V_i \left(\frac{1}{R_2} + \frac{1}{R_1}\right)$  $V_o / V_i = R_2 \left( \frac{1}{R_2} + \frac{1}{R_1} \right)$ Voltage gain,



#### Voltage Follower/ Buffer





Here if R <sub>2</sub> =0, Av=3	1
i.e V <sub>0</sub> =V <sub>i</sub>	

- Used to isolate stages from each other.
- Used as an impedance transformer.
- Reduce power consumption of the source
- Reduce distortion from overloading, distortion and other electromagnetic interference.

#### Summer/Adder



Similarly,

Using KCL at the input node

 $i_1 + i_2 + i_3 - i_4 - 0 = 0$ 

Output voltage

$$V_0 = -R_F \left( \frac{V_{i1}}{R_1} + \frac{V_{i2}}{R_2} + \frac{V_{i3}}{R_3} \right)$$

If  $R_f = R1 = R2 = R3$ Then,  $V_0 = -(V_{i1} + V_{i2} + V_{i3})$ 

> Thus, Output signal is the sum of all input signals.

#### Integrator



# $V_o = -\frac{1}{RC} \int V_{in} dt$

#### Differentiator





B. Rimal	NEC License Exam 12
Which of the following electrical characteristics is not exhibited by an ideal op-amp?	Find the output voltage of an ideal op-amp. If V1 and V2 are the two input voltages
a) Infinite voltage gain	a) VO= V1-V2
b) Infinite bandwidth	b) $VO = A \times (V1 - V2)$
c) Infinite output resistance	c) $VO = A \times (V1 + V2)$
d) Infinite slew rate	d) VO= V1×V2
	A nonvoru h
Answer: c	Answer: b
Explanation: An ideal op-amp exhibits zero output resistance so that output can drive an infinite number of other devices.	Explanation: The output voltage of an ideal op-amp is the product of gain and algebraic difference between the two input voltages.
An ideal op-amp requires infinite bandwidth because	
<ul> <li>a) Signals can be amplified without attenuation</li> </ul>	Which is not the ideal characteristic of an op-amp?
<ul> <li>b) Output common-mode noise voltage is zero</li> </ul>	a) Input Resistance –> 0
<li>c) Output voltage occurs simultaneously with input voltage</li>	b) Output impedance –> 0

c) Bandwidth  $\rightarrow \infty$ 

Answer: a

stage.

d) Open loop voltage gain  $\rightarrow \infty$ 

Explanation: Input resistance is infinite so almost any signal source can drive it and there is no loading of the preceding

changes

d) Output can drive infinite number of device

Answer: a

Explanation: An ideal op-amp has infinite bandwidth. Therefore, any frequency signal from 0 to  $\infty$  Hz can be amplified without attenuation.

# Oscillator

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- Oscillator is an electronic circuit that generates a periodic waveform on its output without an external signal source. It is used to convert dc to ac.
- Oscillators are circuits that produce a continuous signal of some type without the need of an input.
- These signals serve a variety of purposes.
- Communications systems, digital systems (including computers), and test equipment make use of oscillators



## **Basic Principle of Oscillator**

A Basic oscillator contains:

- a frequency selection feedback network
- an amplifier to maintain the loop gain at **unity**
- Vs= input= 0

Vo= Output

A=Gain of amplifier and B gain of +ve feedback

$$V_{o} = AV_{\varepsilon} = A(V_{s} + V_{f})$$
 and  $V_{f} = \beta V_{o}$   
 $\Rightarrow \frac{V_{o}}{V_{s}} = \frac{A}{1 - A\beta}$ 

If  $V_s = 0$ , the only way that  $V_0$  can be nonzero is that loop gain  $A\beta=1$  which implies that

$$|A\beta|=1$$
 (Barkhausen Criterion)  
 $\angle A\beta=0$ 

Fig: An amplifier with positive feedback acts as an oscillator".

Noise signals and the transients associated with the circuit turning on provide the initial source signal that initiate the oscillation



#### **Oscillator Basics**

Oscillators convert dc to	ac
In order for an oscillator to work, the feedback must be	positive
An oscillator can't start unless gain (A) is than feedback fraction (B).	greater
For sinusoidal signal the overall phase shift of the feedback loop should be	0/360/n*360

Which among the following parameters acts as an initiator for the operation of an oscillator in the absence of input signal? a. Noise voltage b. Noise power c. Noise temperature d. All

## **Types of Oscillator**



#### **RC** Oscillator

RC feedback oscillators are generally limited to frequencies of 1 MHz or less.

Two Types

- Wien-bridge
- phase-shift

## **RC wein Bridge Oscillator**

Components:

 Op amp operating at non- inverting mode No phase shift

$$A = 1 + \frac{R_2}{R_1}$$

RC series branch (Zs): R-jXc

$$Z_s = \frac{1 + sRC}{sC}$$
  $Z_p = \frac{R}{1 + sRC}$ 

- RC parallel branch (Zp): -jXc\*R/(R-jXc)
- No phase shift by feedback as well.



#### Wien-bridge Oscillator

• The loop gain for the oscillator is;

$$\mathbf{T}(\mathbf{s}) = \mathbf{A}(\mathbf{s})\boldsymbol{\beta}(\mathbf{s}) = \left(\mathbf{1} + \frac{\mathbf{R}_2}{\mathbf{R}_1}\right) \left(\frac{\mathbf{Z}_p}{\mathbf{Z}_p + \mathbf{Z}_s}\right)$$

• where; 
$$Z_{p} = \frac{R}{1 + sRC}$$
  $Z_{s} = \frac{1 + sRC}{sC}$   
 $T(s) = \left(1 + \frac{R_{2}}{R_{1}}\right) \left[\frac{1}{3 + sRC + (1/sRC)}\right]$   
• For Oscillation frequency, fo/wo

$$\mathsf{T}(\mathsf{j}\omega_0) = \left(1 + \frac{\mathsf{R}_2}{\mathsf{R}_1}\right) \left[\frac{1}{3 + \mathsf{j}\omega_0\mathsf{RC} + (1/\mathsf{j}\omega_0\mathsf{RC})}\right]$$

#### Note: s=jw for frequency response



### Wien-bridge Oscillator

#### Phase Shift Condition

Angle of  $T(j\omega)$  be zero, i.e  $T(j\omega)$  must be real (for zero phase condition), the imaginary component must be zero;

$$j\omega_{0}RC + \frac{1}{j\omega_{0}RC} = 0 \qquad => -1.(\omega_{0}RC)^{2} = -1$$
$$=> j\omega_{0}RC = -\frac{1}{j\omega_{0}RC} \qquad => (\omega_{0}RC)^{2} = 1$$
$$=> (j\omega_{0}RC)^{2} = -1 \qquad => \omega_{0}RC = 1$$
$$=> j^{2}(\omega_{0}RC)^{2} = -1 \qquad => \omega_{0} = \frac{1}{RC}$$

$$T(j\omega_0) = \left(1 + \frac{R_2}{R_1}\right) \left[\frac{1}{3 + j\omega_0 RC + (1/j\omega_0 RC)}\right]$$

#### Magnitude Condition

 $T(j\omega) = 1$  in magnitude

$$1 = \left(1 + \frac{R_2}{R_1}\right) \left(\frac{1}{3+0}\right) = \left(1 + \frac{R_2}{R_1}\right) \left(\frac{1}{3}\right)$$

$$\Rightarrow \frac{R_2}{R_1} = 3 - 1 = 2$$

To ensure oscillation, the ratio  $R_2/R_1$ must be slightly greater than 2.

 $R_2$ 

## **RC wein Bridge Oscillator**

Conclusion

 $\omega = \frac{1}{RC}$  $\frac{R_2}{R_1} = 2$  $\Rightarrow R_2 = 2R_1$ 





$$\beta = \frac{1}{3}$$

#### **RC** Oscillator

A properly designed Wi provides a	C C	sine
The feedback fraction i oscillator is	•	0.333
The feedback circuit in oscillator provides	C	<b>0</b> °
The gain of amplifier in should be at least	wein bridge oscillator	3

The ratio of feedback resistor and other resistor 2 of amplifier in wein bridge oscillator is

### RC Phase-Shift Oscillator

The phase shift oscillator utilizes three RC circuits to provide 180° phase shift that when coupled with the 180° of the op-amp itself provides the necessary feedback to sustain oscillations.

The frequency of sustained oscillations generated depends on the values of R and

C and is given by, 
$$f_r = \frac{1}{2\pi\sqrt{6}RC}$$

At this frequency the gain of the op-amp must be at least 29 to satisfy  $A\beta = 1$ .

Now gain of the op-amp inverting amplifier is given by,

$$|A| \ge \frac{R_f}{R_1} \ge 29$$
 for oscillations

 $R_f \ge 29 R_1$ 

.:.



### Phase shift oscillator

- Total phase shift required= 360
- Amplifier phase shift= 180 (Non Inverting mode of Op Amp or CE configuration of BJT)
- One RC is selected to shift phase by 60 degree.
- For 3 same RC, total phase shift =3\*60=180 degree.
- Total phase shift= 180+180=360





#### RC phase shift oscillators contain a minimum of Phase shift network.

a) 1

- b) 2
- c) 3

d) 0

#### Answer: c

Explanation: RC phase shift oscillator contains a minimum of three phase shift networks. There can be also four (with 45 degree phase shift each) and it increases the stability of oscillation.

Amplifier gain for RC phase shift oscillation, to obey Barkhausen's criteria should be minimum of

b) 4

- c) 10
- d) 29

Answer d

One phase shift network of an RC phase contain

inductor.

a) 1 b) 2 c) 3 d) 0 Answer d

The phase shift network will produce a phase shift of 180 degrees at

- a) Three different frequencies
- b) One frequency

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- c) Two different frequencies
- d) Infinitely many frequencies

Answer: b

Explanation: The phase shift oscillator will produce a phase shift of 180 degrees only at a particular frequency by which it is meant to oscillate.

a) 43

a) 1/(π√6 RC)
b) 2/(π√6 RC)
c) 1/(2π√6 RC)
d) 1/(2√6 RC)

#### Answer: c

Explanation: For an RC phase shift oscillator the frequency formula is  $1/2\pi RC\sqrt{2N}$  where N is the total number of RC stages. For a 3 stage circuit, the frequency is  $1/2\pi RC\sqrt{6}$ .

The feedback factor for RC phase shift oscillator is \_\_\_\_\_

a) 1/18 b) 1/29 c) 1/11 d) 1/33 Answer d

What will be oscillator frequency, if phase shift network of 3stages of RC phase shift oscillator contains a capacitor of 7nF and a resistance of 10K in each stage?

a) 928 Hz b) 1KHz c) 1.2KHz d) 895Hz  $\frac{1}{2\pi\sqrt{6}RC} = \frac{0.065}{RC} = \frac{0.065}{10\times10^3\times7\times10^{-9}} = 928.57$ Hz

#### LC Oscillators

- Use transistors and LC tuned circuits or crystals in their feedback network.
- For generating high-frequency signals, LC Oscillator is preferred. At higher frequencies, better results are obtained with LC-tuned oscillators. The transistors (BJTs and FETs) used in the LC oscillators are used in the frequency range of 100 kHz to 100 GHz.
- Examine Colpitts, Hartley



Fig: LC tank different configuration

- The Colpitts oscillator is a type of oscillator that uses an LC circuit in the feed-back loop.
- The feedback network is made up of a pair of tapped capacitors (C<sub>1</sub> and C<sub>2</sub>) and an inductor L to produce a feedback necessary for oscillations.
- The output voltage is developed across C<sub>1</sub>.
- The feedback voltage is developed across C<sub>2</sub>.



- If we replace BJT by pi model we can get
- KCL at the output node: •



voltage divider produces: •

$$V_{gs} = \left(\frac{\frac{1}{sC_2}}{\frac{1}{sC_2} + sL}\right) \bullet V_o^{-Eq} (2)$$

substitute eq(2) into eq(1):  $V_{o} \left| g_{m} + sC_{2} + (1 + s^{2}LC_{2}) \left( \frac{1}{R} + sC_{1} \right) \right| = 0$ •

• Assume that oscillation has started, then  $Vo \neq 0$ 

$$s^{3}LC_{1}C_{2} + \frac{s^{2}LC_{2}}{R} + s(C_{1} + C_{2}) + \left(g_{m} + \frac{1}{R}\right) = 0$$
  
• Let s=j $\omega$   $\left(g_{m} + \frac{1}{R} + \frac{\omega^{2}LC_{2}}{R}\right) + j\omega\left[(C_{1} + C_{2}) - \omega^{2}LC_{1}C_{2}\right] = 0$ 

- both real & imaginary component must be zero
  - Imaginary component:

$$\omega_{0} = \frac{L}{\sqrt{L\left(\frac{C_{1}C_{2}}{C_{1}+C_{2}}\right)}} - Eq (3)$$

- both real & imaginary component must be zero
  - Imaginary component:

$$\frac{\omega^2 LC_2}{R} = g_m + \frac{1}{R}$$

• Combining Eq(3) and Eq(4):

$$\frac{C_2}{C_1} = g_m R$$

to initiate oscillations spontaneously:

$$g_m R > \left(\frac{C_2}{C_1}\right)$$

- Eq (4)

Oscillation frequency

$$\omega_{0} = \frac{1}{\sqrt{L\left(\frac{C_{1}C_{2}}{C_{1}+C_{2}}\right)}} = \frac{1}{\sqrt{L*Ceq}}$$

· For ossicillator,

$$g_m R > \left(\frac{C_2}{C_1}\right)$$

### Hartley Oscillator

- The Hartley oscillator is almost identical to the Colpitts oscillator.
- The primary difference is that the feedback network of the Hartley oscillator uses tapped inductors (L<sub>1</sub> and L<sub>2</sub>) and a single capacitor C.



Feedback is achieved through the inductive divider circuit used in the Hartley oscillator. The ratio  $L_1/L_2$  determines the feedback ratio and must be adjusted to ensure that oscillations will start.

#### Hartley Oscillator

The analysis of Hartley oscillator is identical to that Colpitts oscillator The frequency of oscillation:

$$\omega_{\rm o} = \frac{1}{\sqrt{(L_1 + L_2)C}} \qquad = \frac{1}{\sqrt{Leq * C}}$$

B. Rimal

A ac circuit contains a capacitor of capacitance 10<sup>-6</sup>F and an inductor of

10<sup>-4</sup>H. The frequency of electrical oscillations will be

1. 10 <sup>5</sup> Hz		
2. 10 Hz		
3. $\frac{10^5}{2\pi}Hz$		
4. $\frac{10}{2\pi}Hz$		

• The resonance frequency is given by

$$\Rightarrow f = rac{1}{2\pi\sqrt{LC}}$$

$$\Rightarrow f = rac{1}{2\pi \sqrt{10^{-6} imes 10^{-4}}} = rac{10^5}{2\pi} Hz$$

Find the angular frequency of the oscillation for the circuit shown in the figure:



Find the true statement. a) When a resistor is connected to an inductor, the electric current in the circuit undergoes LC oscillations b) When a resistor is connected to a capacitor, the electric current in the circuit undergoes LC oscillations c) When a charged capacitor is connected to an inductor, the electric current in the circuit and charge on the capacitor undergoes LC oscillations d) All

Answer C
- Most communications and digital applications require the use of oscillators with extremely stable output. Crystal oscillators are invented to overcome the output fluctuation experienced by conventional oscillators.
- Crystals used in electronic applications consist of a quartz wafer held between two metal plates and housed in a package as shown in Fig. 9 (a) and (b).



- Piezoelectric Effect
  - The quartz crystal is made of silicon oxide (SiO<sub>2</sub>) and exhibits a property called the piezoelectric
  - When a changing an alternating voltage is applied across the crystal, it vibrates at the frequency of the applied voltage. In the other word, the frequency of the applied ac voltage is equal to the natural resonant frequency of the crystal.
  - The thinner the crystal, higher its frequency of vibration. This phenomenon is called piezoelectric effect.

- Characteristic of Quartz Crystal •
  - The crystal can have two resonant frequencies: •
  - 5 One is the series resonance frequency  $f_1$  which occurs • when  $X_1 = X_{c}$ . At this frequency, crystal offers a very  $\neq c$ low impedance to the external circuit where Z = R.
  - The other is the parallel resonance (or antiresonance) • frequency f<sub>2</sub> which occurs when reactance of the series leg equals the reactance of  $C_{M}$ . At this frequency, crystal offers a very high impedance to the external circuit



$$f_{\rm S} = \frac{1}{2\pi \sqrt{L_{\rm S}C_{\rm S}}} \qquad f_{\rm P}$$

• The crystal is connected as a series element in the feedback path from collector to the base so that it is excited in the series-resonance mode



- Hartley and Colpitts's oscillators are LC oscillators.
- LC oscillators are unstable oscillators.

(a)

- Phase shift oscillator is suitable for oscillations in AF range up to 1 kHz
- Crystals like quartz have high-quality factors, Q (range: 10<sup>4</sup> 10<sup>6</sup>). The highquality factor will result in high-frequency stability.

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(b)

- Since, in series resonance, crystal impedance is the smallest that causes the crystal provides the largest positive feedback.
- Resistors R<sub>1</sub>, R<sub>2</sub>, and R<sub>E</sub> provide a voltage-divider stabilized dc bias circuit. Capacitor C<sub>E</sub> provides ac bypass of the emitter resistor, R<sub>E</sub> to avoid degeneration.
- The RFC coil provides dc collector load and also prevents any ac signal from entering the dc supply.
- The coupling capacitor C<sub>C</sub> has negligible reactance at circuit operating frequency but blocks any dc flow between collector and base.
- The oscillation frequency equals the series-resonance frequency of the crystal and is given by:

$$f_{o} = \frac{1}{2\pi\sqrt{LC_{c}}}$$

#### B. Rimal

A quartz crystal has the following values:  $Rs = 6.4\Omega$ , Cs = 0.09972pF and Ls =2.546mH. Calculate the fundamental oscillating frequency of the crystal.

$$f_{\rm S} = \frac{1}{2\pi\sqrt{L_{\rm S}C_{\rm S}}} = \frac{1}{2\pi\sqrt{2.546\,{\rm m\,H}\times0.09972\rm pF}}$$

$$f_{\rm S} = \frac{1}{2\pi\sqrt{0.002546 \times 99.72 \times 10^{-15}}} = 9.987\,\rm{MHz}$$

NEC License Exam42Like which of the following circuit does aquartz oscillator behaves like at stableresonant based frequency?

- a) RC
- b) RLC
- c) LC
- d) RL

### Ans: b

A quartz crystal has \_\_\_\_\_ value of Q factor?

- a) Greater
- b) Lesser
- c) Infinite
- d) Zero

### Answer a

B. Rimal	NEC License Exam	43
A crystal oscillator generates electrical oscillation of constant frequency based on the effect.	The oscillator that gives good frequen	cy stability is
1. ultrasonic	1. Harley Oscillator	
2. magnetic	2. Colpitts Oscillator	
3. piezoelectric	3. Crystal oscillator	
4. photoelectric	4. RC phase shift oscillator	
In a crystal oscillator, a crystal has thickness of t, If you reduce t by 1%, what happens to the frequency 'f'?		
1. f will increase by 2%	In a crystal oscillator, the frequency(f) of the oscillator is inversely	
2. f will decrease by 2%	the thickness(t) of the crystal, $f \propto rac{1}{t}$	

- 3. f will increase by 1%
- 4. f will decrease by 1%

### Wave Generator

A waveform generator is an electronic circuit, which generates a standard wave. There are two types of op-amp based waveform generators –

- Square wave generator
- Triangular wave generator

### Square wave generator Construction

A square wave generator is an electronic circuit which generates square wave.

Observe that in the circuit diagram shown above, the resistor R<sub>1</sub> is connected between the inverting input terminal of the op-amp and its output of op-amp. So, the resistor R<sub>1</sub> is used in the **negative feedback**. Similarly, the resistor R<sub>2</sub> is connected between the noninverting input terminal of the op-amp and its output. So, the resistor R<sub>2</sub> is used in the **positive feedback** path.

A capacitor C is connected between the inverting input terminal of the op-amp and ground. So, the **voltage across capacitor C** will be the input voltage at this inverting terminal of op-amp. Similarly, a resistor  $R_3$  is connected between the non-inverting input terminal of the op-amp and ground. So, the **voltage across resistor**  $R_3$  will be the input voltage at this non-inverting terminal of the op-amp.



# Working

**B.** Rimal

•Assume, there is **no charge** stored in the capacitor initially. Then, the voltage present at the inverting o terminal of the op-amp is zero volts. But, there is some offset voltage at non-inverting terminal of op-amp.<sup>V<sub>Sat</sub> · Due to this, the value present at the output of above circuit will be +Vsat.</sup>



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•Now, the capacitor C starts **charging** through a resistor R1. The value present at the output of the above circuit will change to –Vsat, when the voltage across the capacitor C reaches just greater than the voltage (positive value) across resistor R3.

•The capacitor C starts **discharging** through a resistor R1, when the output of above circuit is –Vsat. The value present at the output of above circuit will change to +Vsat, when the voltage across capacitor C reaches just less than (more negative) the voltage (negative value) across resistor R3.

The output of the op-amp is forced to swing repetitively between positive saturation, +Vsat and negative saturation, -Vsat.

# **Important Conclusion**

- 1 Op amp
- 1 Capacitor

### 3 Resistors:

R1 is for negative feedback

R2 is for positive feedback

R3 between the non-inverting input terminal of the op-amp and ground. So, the voltage across resistor R3 will be the input voltage Expression for period is

$$T = 2RC \ln \frac{1+\beta}{1-\beta}$$
 where  $\beta = \frac{R_2}{R_1+R_2}$ 

If  $R_1 = R_2$ , the equation for period reduces to  $T = 2RC \ln 3$ 

The frequency of oscillation,  $f = \frac{1}{2RC\ln 3}$ 



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Fig 1. Square wave generator and waveforms

# Triangular Wave Generator

 A triangular wave generator is an electronic circuit, which generates a triangular wave.



# MCQs

- How are the square wave output generated in opamp?
- a) Op-amp is forced to operate in the positive saturation region
- b) Op-amp is forced to operate in the negative saturation region
- c) Op-amp is forced to operate between positive and negative saturation region
- d) None of the mentioned

#### Answer: c

Explanation: Square wave outputs are generated where the op-amp is forced to operate in saturated region, that is, the output of the op-amp is forced to swing repetitively between positive saturation, +Vsat and negative saturation, -Vsat. Determine the expression for time period of a square wave generator a) T= 2RC  $ln \times [(R1+R2) / (R2)]$ . b) T= 2RC  $ln \times [(2R1+R2) / (R2)]$ . c) T= 2RC  $ln \times [(R1+2R2) / (R2)]$ . d) T= 2RC  $ln \times [(R1+R2) / (2R2)]$ .

### Answer: b

Explanation: The time period of the output waveform for a square wave generator is  $1/f = T = 2RC \ln (2R1 + R2)/(R2)$ ].

### Determine the output frequency for the



Answer: d Explanation: The output frequency  $f_0 = 1/2RC \times ln [(2R_1 + R_2)/R_2]$ = 1 / {(2×33k $\Omega \times 0.33\mu$ F)×ln[(2×33k $\Omega$ +30k $\Omega$ )/30k $\Omega$ ]} = 1/ (0.02175×ln 32) = 39.47 Hz. **NEC License Exam** 

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