

Multiple Choice Questions

- 1) The stalling of the processor due to the unavailability of the instructions is called as _____
a) Control hazard b) structural hazard c) Input hazard d) None of the mentioned
- 2) Which of the following is not considered as a peripheral device?
a) CPU b) Keyboard c) Monitor d) All of the above
- 3) Which of the following computer memory is fastest?
a) Register b) Hard disk c) RAM d) None of the above
- 4) Which of the following operations is/are performed by the ALU?
a) Data manipulation b) Exponential c) Square root d) All of the above
- 5) Which of the following format is used to store data?
a) Decimal b) Octal c) BCD d) Hexadecimal
- 6) Which of the following memory of the computer is used to speed up the computer processing?
a) Cache memory b) RAM c) ROM d) none of the above
- 7) Who developed the basic architecture of computer?
a) Blaise Pascal b) Charles Babbage c) John Von Neumann d) None of the above
- 8) Computer address bus is
a) Multidirectional b) Bidirectional c) Unidirectional d) None of the above
- 9) Which of the following circuit is used to store one bit of data?
a) Flip Flop b) Decoder c) Encoder d) Register
- 10) Which of the following is a way in which the components of a computer are connected to each other?
a) Computer parts b) Computer architecture c) Computer hardware d) None of the above
- 11) The address in the main memory is known as –
a) Logical address b) Physical address c) Memory address d) None of the above
- 12) Subtraction in computers is carried out by –
a) 1's complement b) 2's complement c) 3's complement d) 9's complement
- 13) Which of the following memory unit communicates directly with the CPU?
a) Auxiliary memory b) Main memory c) Secondary memory d) None of the above
- 14) The collection of 8-bits is called as -.
a) Byte b) Nibble c) Word d) Record
- 15) Which of the following is a group of bits that tells the computer to perform a particular operation?
a) Accumulator b) Register c) Instruction code d) None of the above
- 16) Where is the document temporarily stored during working on a document on PC?
a) ROM b) CPU c) RAM d) Flash memory
- 17) Where is the decoded instruction stored?
a) Registers b) MDR c) PC d) IR

- 18) The status bit is also called as –
a) Unsigned bit b) Signed bit c) Flag bit d.) None of the above
- 19) Which of the following register can interact with the secondary storage?
a) PC b) MAR c) MDR d) IR
- 20) In memory-mapped I/O _____
a) The I/O devices and the memory share the same address space
b) The I/O devices have a separate address space
c) The memory and I/O devices have an associated address space
d) A part of the memory is specifically set aside for the I/O operation
- 21) The contention for the usage of a hardware device is called ____
a) Structural hazard b) Stalk c) Deadlock d) None of the mentioned
- 22) The usual BUS structure used to connect the I/O devices is _____
a) Star BUS structure b) Multiple BUS structure c) Single BUS structure d) Node to Node BUS structure
- 23) In which of the following term the performance of cache memory is measured?
a) Chat ratio b) Hit ratio c) Copy ratio d) Data ratio
- 24) Which of the following is the function of the control unit in the CPU?
a) It stores program instruction b) It decodes program instruction
c) It performs logic operations d) None of the above
- 25) Which of the following computer register collects the result of computation?
a) Accumulator b) Instruction Pointer c) Storage register d) None of the above
- 26) Which of the following building block can be used to implement any combinational logic circuit?
a) AND b) OR c) NAND d) None of the above
- 27) What does a computer bus line consists of?
a) Set of parallel lines b) Accumulators c) Registers d) None of the above
- 28) Which of the following is performed by half adder?
a) Binary addition operation for 2 decimal inputs
b) Binary addition operation for 2 binary inputs
c) Decimal addition operation for 2 decimal inputs
d) Decimal addition operation for 2 binary inputs
- 29) Circuits that can hold their state as long as power is applied is ____
a) Dynamic memory b) Static memory c) Register d) Cache
- 30) The minimum time delay between two successive memory read operations is ____
a) Cycle time b) Latency c) Delay d) None of the mentioned